

Image

Receipt



Docket No.: 43890-555

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of

Toyonori KANETAKA, et al.

Serial No.: 10/009,750

Filed: December 12, 2001

For: METHOD OF MANUFACTURING CHIP INDUCTOR

: Customer Number: 20277  
:  
: Confirmation Number: 1263  
:  
: Group Art Unit: 1712  
:  
: Examiner: To be Assigned  
:

THIRD REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OFR  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Independent claims data is incorrect. The independent claims data should read: 2. Additionally, the third entry of the Foreign Priority information is incorrect. Attached is a copy of the Declaration and Power of Attorney Document, which evidences this information should read: JAPAN 2000-110263 04/12/2000, JAPAN 2000-110264 04/12/2000, JAPAN 2000-110265 04/12/2000, JAPAN 2000-110266 04/12/2000. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/009,750	04/30/2002	1712	1308	43890-555	16	36	1

20277  
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JUN 14 2002

McDermott, Will &amp; Emery

CONFIRMATION NO. 1263

REPLACEMENT FILING RECEIPT



\*OC00000008261944\*

Date Mailed: 06/11/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP01/03149 04/12/2001

## Foreign Applications

JAPAN 2000-110263 04/12/2000  
JAPAN 2000-110264 04/12/2000  
~~JAPAN 2000-100165 04/12/2000~~  
JAPAN 2000-110266 04/12/2000

JAPAN 2000-110265 4/12/2000

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

**Title**

Method of manufacturing chip inductor

**Preliminary Class**

438

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